

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: N. KOBAYASHI, et al.

Application No.: Rule 1.53(b) Divisional of U.S. Patent Application Serial
No. 10/260,407, filed October 1, 2002

Filed: On even date herewith

For: PROGRAMMING METHOD OF NONVOLATILE
SEMICONDUCTOR MEMORY DEVICE

Art Group of Parent: 2818

Examiner of Parent: C. C. Yoha

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 21, 2003

Sir:

Prior to examination, please amend the above-identified application as listed
below and as set forth on the following pages:

Amendments to the Specification;

Amendments to the Claims; and

Remarks are included following the amendments.